## AMENDMENTS TO THE SPECIFICATION

Please replace paragraph 38 with the following paragraph 38:

[0038]     Figure 6 illustrates a block diagram of a <u>CMOS</u> system in which an NROM array is embedded. This system is comprised of a microprocessor 601 ~~and~~ <u>coupled to</u> the NROM array 602 <u>over CONTROL, ADDRESS, and DATA buses</u>. These components 601 and 602 are incorporated onto a single integrated circuit die. Alternate embodiments may add additional components such as input/output circuitry and other types of memory.